

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	20	(distance or spac\$3) same separat\$4 same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 12:14
L2	0	(distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and "716"/\$.ccls. and (CMOS same separat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:48
L3	0	(distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and "716"/\$.ccls. and (CMOS same (separat\$4 or "U"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:49
L4	1063	(distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and (CMOS same (separat\$4 or "U"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:50
L5	124	(distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) same (CMOS same (separat\$4 or "U"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 08:00
L6	27	(distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) same (CMOS same (separat\$4 or "U")) and cellular	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:56
L7	0	((latch adj up) or latchup) same (check\$3 or analyz\$3 or analysis) same CMOS same (separat\$3 or "U") and (distance or spac\$3 or constraint or drc or (design adj rule))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 08:03
L8	218	((latch adj up) or latchup) same CMOS same (separat\$3 or "U") and (distance or spac\$3 or constraint or drc or (design adj rule))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 08:03
L9	13	((latch adj up) or latchup) same CMOS same (separat\$3 or "U") and (distance or spac\$3 or constraint or drc or (design adj rule)) and (raster\$3 or (bit adj map\$4) or bitmap\$4 or pixel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 09:59

## EAST Search History

L19	2	((latch adj up) or latchup) same CMOS same (separat\$3 or "U") and (spac\$3 near3 rule) and (raster\$3 or (bit adj map\$4) or bitmap\$4 or pixel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 10:04
L20	12	(spac\$3 with rule with (cmos or (well adj contact) or (N adj2 well) or (N adj2 contact) or leg)) and (raster\$7 or pixel or (bit adj2 map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 10:20
L21	5	(spac\$3 with rule with cmos) and (raster\$7 or pixel or (bit adj2 map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:07
L22	20	((latch adj up) or latchup) with spac\$3 with distance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:15
L23	20	((latch adj up) or latchup) with spac\$3 with (distance or unreach\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:16
L24	4	((latch adj up) or latchup) with spac\$3 with \$2reach\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:18
L25	4	((latch adj2 up) or latchup) with spac\$3 with \$2reach\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:19
L26	22	((latch adj2 up) or latchup) same spac\$3 same \$2reach\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 14:41
L27	0	(cellular adj algorithm) and "716."\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 12:14
L28	9	(cellular adj algorithm)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 12:14

## EAST Search History

L29	14	((latch adj2 up) or latchup) same spac\$3) and (raster\$7 or pixel or (bit adj2 map\$3) or bitmap\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 14:51
S1	2	"20050240886"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:18
S2	1	(vector near3 (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and ((area or region or shape) near3 unreachable) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:22
S3	1	(vector near3 (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and ((area or region or shape) near3 (unreachable or unpermit\$4)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:22
S4	1	(vector near3 (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and ((area or region or shape) with (unreachable or unpermit\$4)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:23
S5	1	(vector near3 (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and (unreachable or unpermit\$4) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:23
S6	1	(vector near3 (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and (unreachable or unpermit\$6) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:23
S7	1	(vector near3 (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and (unreachable or unpermit\$6 or proxibit\$6) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:24
S8	1	(vector same (graphic\$3 or imag\$3)) same (bitmap near3 (imag\$3 or format\$4)) and ((rule near3 check\$3) or drc) and (unreachable or unpermit\$6 or proxibit\$6) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:25

## EAST Search History

S9	195	raster\$5 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:25
S10	10728	raster\$5 and drcand "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:25
S11	26	raster\$5 and drc and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:27
S12	26	(raster\$5 or (convert\$3 with (graphic\$3 or imag\$3) with bitmap)) and drc and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:51
S13	26	(raster\$5 or (convert\$3 with ((vector adj imag\$3) or shape or (scalable adj object)) with (bitmap near3 imag\$3))) and drc and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:10
S14	1	(conductor adj region) with (contact adj shape) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:08
S15	2	(conductor near3 region) same (contact near3 shape) and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:12
S16	26	(raster\$5 or (convert\$3 same ((vector near3 imag\$3) or shape or (scalable adj object)) with (bitmap near3 imag\$3))) and drc and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:11
S17	26	(raster\$5 or (convert\$3 same ((vector near3 (imag\$3 or graphic\$3)) or shape or (scalable adj object)) with (bitmap near3 imag\$3))) and drc and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:12
S18	74	(raster\$5 or (convert\$3 same ((vector near3 (imag\$3 or graphic\$3)) or shape or (scalable adj object)) with (bitmap near3 imag\$3))) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 11:08

## EAST Search History

S19	16	(conductor near3 region) same (contact near3 shape)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:13
S20	1	((conductor near3 region) same (contact near3 shape)) and raster\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:13
S21	1	(raster\$5 or (convert\$3 same ((vector near3 (imag\$3 or graphic\$3)) or shape or (scalable adj object)) with (bitmap near3 imag\$3))) and drc and ((latch adj2 up) or latchup or thyristor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 11:10
S22	2	(raster\$5 or (convert\$3 same ((vector near3 (imag\$3 or graphic\$3)) or shape or (scalable adj object)) with (bitmap near3 imag\$3))) and drc and ((latch adj2 up) or latchup or thyristor or (current near3 leak\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:01
S23	1	(raster\$5 or bitmap\$4 or pixel) same layout and drc and ((latch adj2 up) or latchup or thyristor or (current near3 leak\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:16
S24	21	(raster\$5 or bitmap\$4 or pixel) same layout and drc and distance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:17
S25	30	(raster\$5 or bitmap\$4 or pixel) same layout and drc and (distance or spac\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:14
S26	27	(raster\$5 or bitmap\$4 or pixel) same layout and drc and (distance or spac\$3) and (cellular or cell or fill\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:23
S27	1	(raster\$5 or bitmap\$4 or pixel) and layout and drc and (distance or spac\$3) and (cellular near3 algorithm)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:24

## EAST Search History

S28	95	(raster\$5 or bitmap\$4 or pixel) and (cellular near3 algorithm)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:24
S29	1	(raster\$5 or bitmap\$4 or pixel) and (cellular near3 algorithm) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:25
S30	112	(raster\$5 or bitmap\$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand\$3) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:26
S31	105	(raster\$5 or bitmap\$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand\$3) and drc and (distance or spac\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:33
S32	29	(raster\$5 or bitmap\$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand\$3) same (distance or spac\$3) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:27
S33	7	(raster\$5 or bitmap\$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand\$3) and drc and (distance or spac\$3) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:45
S34	8	(raster\$5 or bitmap\$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand\$3) and drc and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:25
S35	1	(raster\$5 or bitmap\$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand\$3) and drc and ((latch adj2 up) or thyristor) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:27
S36	1	(raster\$5 or bitmap\$4 or pixel) and drc and ((latch adj2 up) or thyristor) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:27
S37	11	drc and ((latch adj2 up) or thyristor) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:55

## EAST Search History

S38	28	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5) and (pixel or raster\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:08
S39	5	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5)) and (pixel or raster\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:57
S40	132	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5) and (pixel or raster\$3) or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:16
S41	20	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5) and (pixel or raster\$3) or (bit adj map\$4) or bitmap\$4) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:10
S42	5	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5)) and ((pixel or raster\$3) or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:31
S43	5	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5)) and ((pixel or raster\$3) or (bit adj map\$4) or bitmap\$4) and (distance or spac\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:32
S44	5	drc and ((latch adj2 up) or thyristor or (current near3 leak\$5)) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:16
S45	23	drc and (well adj contact) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:19
S46	26	drc and ((well or substrate or butting) adj contact) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:55
S47	215	drc and ("N" adj well) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:01

## EAST Search History

S48	65	drc same ("N" adj well) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:58
S49	5	drc same ("N" adj well) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4) and (expand\$3 or cellular)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:56
S50	3	drc and ("N" adj well) and (pixel or raster\$3 or (bit adj map\$4) or bitmap\$4) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:04
S51	0	drc same CMOS same ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:06
S52	3	drc same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:09
S53	1944	CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:10
S54	373	(drc or (design near3 rule) or constraint) and CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:10
S55	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:11
S56	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:52



## EAST Search History

S57	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and unreachable and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:48
S58	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (thyristor near3 effect\$3) or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and unreachable and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:49
S59	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (thyristor near3 effect\$3) or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and unreachable and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:50
S60	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (thyristor near3 effect\$3) or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "unreachable" and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:51
S61	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "unreachable" and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:51
S62	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls. and unreachable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:53
S63	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls. and "unreachable"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:53

## EAST Search History

S64	5	(drc or (design near3 rule) or constraint) same CMOS same (dastance or spac\$3) and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:08
S65	6	(drc or (design near3 rule) or constraint) same CMOS same (distance or spac\$3) and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:14
S66	138	(drc or (design near3 rule) or constraint) same (distance or spac\$3) and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:14
S67	138	(drc or (design near3 rule) or constraint) same (distance or spac\$3) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:15
S68	138	(drc or (design near3 rule) or constraint) same (distance or spac\$3) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and "716"/\$.ccls. and CMOS and geometr\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:15
S69	1	(drc or (design near3 rule) or constraint) same (distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 15:32

## EAST Search History

S70	1	(drc or (design near3 rule) or constraint or (layout near3 (verif\$3 or verification or test\$3 or check\$3 or evaluat\$3))) same (distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 15:36
S71	7	(drc or (design near3 rule) or constraint or (layout near3 (verif\$3 or verification or test\$3 or check\$3 or evaluat\$3))) same (distance or spac\$3) and (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4)) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 17:09
S72	0	bitmap with drc and cmos	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 16:32
S74	1	((distance or spac\$3) with greater) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 17:15
S75	12	(distance or spac\$3) same (raster\$5 or pixel or (bitmap\$4 or (bit adj map\$4))) and "716"/\$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:34


[Home](#) | [Login](#) | [Logout](#) | [Access Inform](#)

Welcome United States Patent and Trademark Office

## Search Results

[BROWSE](#)
[SEARCH](#)
[IEEE XPLORE GUIDE](#)

Results for "((raster\* and (drc or constraint or (spac\* rule) or (design rule)))<in>metadata)"

Your search matched **21** of **1351636** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

## » Search Options

[View Session History](#)
[New Search](#)

## Modify Search

((raster\* and (drc or constraint or (spac\* rule) or (design rule)))<in>metad

☐ Check to search only within this results set

Display Format:

☒ Citation ☐ Citation & Abstract

## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

[view selected items](#)

[Select All](#) [Deselect All](#)

- ☐ **1. A high quality vectorization combining local quality global constraints**  
Roosli, M.; Monagan, G.;  
Document Analysis and Recognition, 1995., Proceedi  
International Conference on  
Volume 1, 14-16 Aug. 1995 Page(s):243 - 248 vol.1  
Digital Object Identifier 10.1109/ICDAR.1995.598986  
AbstractPlus | Full Text: PDF(652 KB) IEEE CNF  
Rights and Permissions
- ☒ **2. A Deterministic Finite Automaton Approach to Design Checking for VLSI**  
Eustace, R.A.; Mukhopadhyay, A.;  
Design Automation, 1982. 19th Conference on  
14-16 June 1982 Page(s):712 - 717  
AbstractPlus | Full Text: PDF(504 KB) IEEE CNF  
Rights and Permissions
- ☐ **3. The TFTR neutral beam pulse length limit**  
Kamperschroer, J.H.; Garzotto, V.; Grisham, L.R.; Mc  
O'Connor, T.E.; Oldaker, M.E.; Stevenson, T.N.; Von  
Fusion Engineering, 1997. 17th IEEE/NPSS Symposi  
Volume 2, 6-10 Oct. 1997 Page(s):1079 - 1082 vol.2  
Digital Object Identifier 10.1109/FUSION.1997.68781  
AbstractPlus | Full Text: PDF(396 KB) IEEE CNF  
Rights and Permissions
- ☐ **4. A Hardware Assisted Design Rule Check Architect**  
Seiler, L.;  
Design Automation, 1982. 19th Conference on  
14-16 June 1982 Page(s):232 - 238  
AbstractPlus | Full Text: PDF(832 KB) IEEE CNF  
Rights and Permissions

- ☐ **5. Analysis and comparison of declustering scheme: navigation queries**  
Chen, C.-M.; Sinha, R.K.;  
Knowledge and Data Engineering, IEEE Transactions  
Volume 12, Issue 5, Sept.-Oct. 2000 Page(s):763 - 7  
Digital Object Identifier 10.1109/69.877507  
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(376 KB\)](#)  
[Rights and Permissions](#)
  
- ☐ **6. A novel approach to real-time bilinear interpolation**  
Gibbon, K.T.; Bailey, D.G.;  
Electronic Design, Test and Applications, 2004. DELTA  
IEEE International Workshop on  
28-30 Jan. 2004 Page(s):126 - 131  
Digital Object Identifier 10.1109/DELTA.2004.10055  
[AbstractPlus](#) | [Full Text: PDF\(160 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **7. Raster-spatial data declustering revisited: an inter navigation perspective**  
Chung-Min Chen; Sinha, R.K.;  
Data Engineering, 1999. Proceedings., 15th Internatic  
on  
23-26 March 1999 Page(s):600 - 607  
Digital Object Identifier 10.1109/ICDE.1999.754976  
[AbstractPlus](#) | [Full Text: PDF\(136 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **8. A space saving approach to the Hough transform**  
Albanesi, M.G.; Ferretti, M.;  
Pattern Recognition, 1990. Proceedings., 10th Interna  
Conference on  
Volume ii, 16-21 June 1990 Page(s):472 - 475 vol.2  
Digital Object Identifier 10.1109/ICPR.1990.119403  
[AbstractPlus](#) | [Full Text: PDF\(420 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **9. Type design with flexible constrained font charact**  
Hussain, F.; Zalik, B.;  
Document Image Processing and Multimedia (Ref. Nc  
Colloquium on  
25 March 1999 Page(s):6/1 - 614  
[AbstractPlus](#) | [Full Text: PDF\(732 KB\)](#) IEE CNF
  
- ☐ **10. On two-dimensional maximum entropy spectral e**  
Srinivasa, N.; Ramakrishnan, K.R.; Rajgopal, K.;  
Signal Processing, IEEE Transactions on [see also A  
Speech, and Signal Processing, IEEE Transactions c  
Volume 40, Issue 1, Jan. 1992 Page(s):241 - 244  
Digital Object Identifier 10.1109/78.157205  
[AbstractPlus](#) | [Full Text: PDF\(332 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ **11. A bit-serial VLSI architecture for generating mom**  
Liu, W.; Chen, S.-S.; Cavin, R.;  
Systems, Man and Cybernetics, IEEE Transactions o  
Volume 23, Issue 2, March-April 1993 Page(s):539 -  
Digital Object Identifier 10.1109/21.229466

[AbstractPlus](#) | [Full Text: PDF\(620 KB\)](#) [IEEE JNL](#)  
[Rights and Permissions](#)

- ☐ **12. Ringtree: a VLSI architecture for fast image gener processing**  
 Eo, K.S.; Kim, S.S.; Kyung, C.M.;  
Circuits and Systems, 1988. IEEE International Sym  
7-9 June 1988 Page(s):801 - 804 vol.1  
 Digital Object Identifier 10.1109/ISCAS.1988.15046  
[AbstractPlus](#) | [Full Text: PDF\(360 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **13. Exploiting bit level concurrency in real-time geon extractions**  
 Liu, W.; Yeh, T.-F.; Batchelor, W.E.; Cavin, R.;  
Computer Architecture, 1988. Conference Proceedin  
International Symposium on  
30 May-2 June 1988 Page(s):167 - 174  
 Digital Object Identifier 10.1109/ISCA.1988.5226  
[AbstractPlus](#) | [Full Text: PDF\(604 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **14. A sorting classification of parallel rendering**  
 Molnar, S.; Cox, M.; Ellsworth, D.; Fuchs, H.;  
Computer Graphics and Applications, IEEE  
Volume 14, Issue 4, July 1994 Page(s):23 - 32  
 Digital Object Identifier 10.1109/38.291528  
[AbstractPlus](#) | [Full Text: PDF\(1008 KB\)](#) [IEEE JNL](#)  
[Rights and Permissions](#)
  
- ☐ **15. Study of topological relations between vague regi space based on rough sets**  
 Zhenji Gao; Yu Liu; Yueyong Zhou; Shi Qin;  
Geoscience and Remote Sensing Symposium, 2005.  
Proceedings. 2005 IEEE International  
Volume 1, 25-29 July 2005 Page(s):4 pp.  
 Digital Object Identifier 10.1109/IGARSS.2005.15262  
[AbstractPlus](#) | [Full Text: PDF\(276 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **16. VLSI implementation of real-time image rotation**  
 Bhandakar, S.M.; Huaiyuan Yu;  
Image Processing, 1996. Proceedings., International  
Volume 1, 16-19 Sept. 1996 Page(s):1015 - 1018 vo  
 Digital Object Identifier 10.1109/ICIP.1996.561079  
[AbstractPlus](#) | [Full Text: PDF\(580 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)
  
- ☐ **17. A proposed extension of the ODA document mod processing of multimedia documents**  
 Lubich, H.P.;  
Communications Software, 1991, 'Communications f  
Applications and Systems', Proceedings of TRICOM  
Conference on  
18-19 April 1991 Page(s):59 - 72  
 Digital Object Identifier 10.1109/TRICOM.1991.1528;  
[AbstractPlus](#) | [Full Text: PDF\(852 KB\)](#) [IEEE CNF](#)  
[Rights and Permissions](#)

- ☐ **18. A Class of Cellular Architectures to Support Phys Automation**  
Rutenbar, R.A.; Mudge, T.N.; Atkins, D.E.;  
Computer-Aided Design of Integrated Circuits and Systems Transactions on  
Volume 3, Issue 4, October 1984 Page(s):264 - 278  
AbstractPlus | Full Text: [PDF\(2480 KB\)](#) IEEE JNL  
Rights and Permissions
  
- ☐ **19. Supporting interactive data exploration for GIS pl with a multi-modal virtual environment**  
Harding, C.; Newcomb, M.;  
Haptic, Audio and Visual Environments and Their Applications HAVE 2004. Proceedings. The 3rd IEEE International  
2-3 Oct. 2004 Page(s):81 - 86  
AbstractPlus | Full Text: [PDF\(788 KB\)](#) IEEE CNF  
Rights and Permissions
  
- ☐ **20. Intelligent digitisation of Arabic characters**  
Hussain, F.; Zalik, B.; Kolmanic, S.;  
Information Visualization, 2000. Proceedings. IEEE International Conference on  
19-21 July 2000 Page(s):337 - 342  
Digital Object Identifier 10.1109/IV.2000.859778  
AbstractPlus | Full Text: [PDF\(436 KB\)](#) IEEE CNF  
Rights and Permissions
  
- ☐ **21. Closed-form 2D angle estimation with arbitrary sa over a circular aperture**  
Ramos, J.; Zoltowski, M.D.;  
Acoustics, Speech, and Signal Processing, 1996. ICASSP Conference Proceedings., 1996 IEEE International Conference  
Volume 5, 7-10 May 1996 Page(s):2571 - 2574 vol. 5  
Digital Object Identifier 10.1109/ICASSP.1996.54798  
AbstractPlus | Full Text: [PDF\(324 KB\)](#) IEEE CNF  
Rights and Permissions

Help Cont  
Se

© Copyright

Indexed by  
 Inspec